



#6/A
1/5/02
501.39868X00
Parrison

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: SAITO et al.
Serial No.: 09/850,162
Filed: May 8, 2001
For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A
METHOD OF MANUFACTURING THE SAME
Group: 2812
Examiner: L. Gurley

AMENDMENT

Assistant Commissioner
for Patents
Washington, D.C. 20231

December 26, 2001

Sir:

In response to the Office Action dated October 26, 2001,
the period of response for which extension is requested, the
attached Petition for Extension of Time, please amend the

above-identified application as follows:

In the Claims:

Please amend claims 6 and 7 as follows:

6. (Amended) A method for manufacturing a semiconductor
integrated circuit device according to Claim 1, 4 or 5 further
comprising the steps of:

partly removing said second insulating film defined in
Claim 1, said third insulating film defined in Claim 4 or said

RECEIVED
DEC 31 2001
MAIL ROOM

sub
C1
com